

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10026245	FILING DATE 12/17/2001	CLASS 703	SUBCLASS 18	GAU 2123	EXAMINER <i>BRUDA, JAY Fennis</i>
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**APPLICANTS: Becker Scott;

****CONTINUING DATA VERIFIED:**

THIS APPLICATION IS A CON OF 09/442,877 11/18/1999
WHICH IS A DIV OF 09/015,427 01/29/1998 PAT 6,016,390

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO ARTCP012C
TITLE : Memories having reduced bitline voltage offsets L. M. Fennis U.S. DEPT. OF COMM./PAT & TM-PTO-438L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 0	Print Claim for 0.0
Assistant Examiner		DRAWING	
		Sheets Drawn 0	Page Drawn 0
		Print File	
ISSUE FEE			
Amount Due	Date Paid		
TERMINAL			
DISCLAIMER			
PREPARED FOR ISSUE			
Primary Examiner Application Examiner			
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